AN EXPLICIT MODEL OF THE MOS TRANSISTOR FOR DESIGN AND SIMULATION OF ANALOG CIRCUITS

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Abstract - This paper presents an explicit analytical model of the long-channel MOS transistor valid in all regions of operation - weak, moderate and strong inversion. The drain current, total charges and small-signal parameters for quasi-static operation are expressed as very simple functions of the inversion charge densities at the channel ends. The charge densities, in turn, are formulated as explicit continuous functions of the terminal voltages, with continuous first order derivatives. The proposed model is precise and very simple to be incorporated in CAD programs or to be used in designs by hand. Moreover, our model contains only the classical parameters of the MOSFET theory.

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I - Introduction

Despite the existence of several MOSFET models, none of them includes simple formulae to describe adequately the transistor characteristics in all operating regions [1]. Models based on the classical linear approximation of the inversion charge density in strong inversion are often used in CAD programs. Nevertheless, they do not express satisfactorily the MOSFET behavior [2], mainly in the so-called "moderate inversion region".

This paper presents an explicit model of the long-channel MOS transistor valid in all regions of operation. Our model is based on an approximation of the semiconductor capacitance [3], which allows determining an explicit expression for the inversion charge density in terms of the terminal voltages. The MOSFET static and dynamic characteristics have an explicit formulation in terms of the inversion charge densities at the source and drain ends.

II - Approximation of the Semiconductor Capacitance

Our model is based on an approximation of the semiconductor capacitance per unit area, C'_c, in inversion. This capacitance is usually split into two components [1], the inversion (C'_i) and depletion (C'_b) capacitances:

$$C'_c = C'_i + C'_b, \tag{1}$$

which we have written as functions of the inversion (Q'_i) and depletion (Q'_B) charge densities:

$$C'_i = \frac{|Q'_i|}{2\phi_t}\left(1+\frac{Q'_B}{Q'_C}\right) \tag{2.a}$$

$$C'_b = \frac{\gamma^2 C'^2_{ox}}{2|Q'_C|} \tag{2.b}$$

Here ϕ_t is the thermal voltage, C'_{ox} is the oxide capacitance per unit area, γ is the body effect factor and Q'_C is the semiconductor charge density given by:

$$Q'_C = Q'_i + Q'_B \tag{2.c}$$

Eqn.(2.a) allows achieving an explicit formulation of the inversion capacitance from conventional approximations of the semiconductor charges. We use here the linear function of the channel potential V_{CB}, valid in strong inversion:

$$-Q_i' = C_{ox}' n(V_P - V_{CB}) - Q_{IP}' \qquad \text{for } V_{CB} \leq V_P \quad (3.a)$$

Q_{IP}' is the value of the inversion charge density in the upper limit of weak inversion wich has been included in the formula presented in refs.[4, 5] in order to obtain a continuous transition for Q_i', from strong to weak inversion. In eqn.(3.a), V_P (Table 2) is the pinch-off voltage [4, 5] that can be approximated by [5]

$$V_P \cong \frac{V_{GB} - V_{T0}}{n} \qquad (3.b)$$

where V_{T0} is the value of the threshold voltage for $V_{CB} = 0$ and n (Table 2) is the slope factor.

Substituting eqn.(3.a) into eqn.(2.a), we approximate the inversion capacitance in strong inversion, including the so-called moderate inversion region, by:

$$C_i' = \frac{C_{ox}'}{2\phi_t} n(V_P - V_{CB})\left(1 + \frac{Q_B'}{Q_C'}\right) - \frac{Q_{IP}'}{2\phi_t}\left(1 + \frac{Q_B'}{Q_C'}\right) \qquad \text{for } V_{CB} \leq V_P \quad (4)$$

The first term in the right hand side of eqn.(4) prevails deep in strong inversion, where the term $1+Q_B'/Q_C'$ can be approximated to 1. The second term predominates near threshold, where the term $1+Q_B'/Q_C'$ can be better approximated to 2. Assuming that in strong inversion $C_c' \cong C_i'$, we can, therefore, write:

$$C_c' \cong C_i' \cong C_{ox}' n\frac{(V_P - V_{CB})}{2\phi_t} + (n-1)C_{ox}' \quad , \qquad \text{for } V_{CB} \leq V_P \quad (5.a)$$

which is a slight modification of the semiconductor capacitance model used in ref.[3] to determine the harmonic distortion in MOS gate capacitors in strong inversion. Eqn.(5.a) represents the fundamental approximation to be used throughout this work in order to achieve explicit expressions that represent the static and dynamic behavior of the MOS transistor. The term $(n-1)C_{ox}'$ guarantees continuity between eqn.(5.a) and the classical approximation of the semiconductor capacitance in weak inversion:

$$C_c' \cong C_b' \cong \frac{\gamma}{2\sqrt{2\phi_F + V_P}} C_{ox}' = (n-1)C_{ox}' \qquad \text{for } V_{CB} \geq V_P \quad (5.b)$$

Fig.1 shows the values of the gate capacitance (C'_g),

$$\frac{1}{C'_g} = \frac{1}{C'_c} + \frac{1}{C'_{ox}}, \tag{6}$$

computed from the explicit expression of the semiconductor capacitance, eqns.(5), from the theoretical implicit expression of C'_c [1] and from the usual approximation assuming infinite semiconductor capacitance for strong inversion.

III - Approximation of the Inversion Charge Density

The inversion charge density Q'_i can be determined from:

$$Q'_i = Q'_{IP} + \int_{V_P}^{V_{CB}} \frac{dQ'_i}{dV_{CB}} dV_{CB} \tag{7.a}$$

where

$$\frac{dQ'_i}{dV_{CB}} = \left(1 + \frac{\gamma}{2\sqrt{\phi_S}}\right) \frac{C'_i}{1 + C'_c / C'_{ox}} \tag{7.b}$$

and ϕ_S is the surface potential.

Substituting eqn.(7.b) into eqn.(7.a) and approximating ϕ_S by its value $2\phi_F + V_P$ at pinch-off, we obtain:

$$Q'_i = Q'_{IP} + n \int_{V_P}^{V_{CB}} \frac{C'_i}{1 + C'_c / C'_{ox}} dV_{CB} \tag{7.c}$$

In order to evaluate the integral in eqn.(7.c) we have used the strong inversion approximation of C'_c and C'_i given by eqn.(5.a), which leads to the expression of Q'_i presented in Table 1. For weak inversion, we have adopted here the conventional expression for the inversion charge density [1, 4, 5], also presented in Table 1.

The expressions of Q'_i in Table 1, in weak and strong inversion, represent a piecewise but smooth function. They provide a continuous transition from weak to strong inversion as well as from conduction to saturation for Q'_i and its first order partial derivatives with respect to V_{GB} and V_{CB} (Table 1).

IV - Drain Current and Total Charges

A linearization of $Q_I'(\phi_S)$ about $2\phi_F + V_P$ gives:

$$\frac{dQ_I'}{d\phi_S} = nC_{ox}' \tag{8}$$

The use of eqn.(8) allows changing the variable ϕ_S to Q_I' in the integral expression of the drain current [1]:

$$I_D = -\frac{\mu}{nC_{ox}'}\frac{W}{L}\int_{Q_{IS}}^{Q_{ID}}(Q_I' - nC_{ox}'\phi_t)dQ_I' \tag{9}$$

where Q_{IS}' and Q_{ID}' are the inversion charge densities at the source and drain ends, respectively, μ is the carrier mobility, W is the channel width and L is the channel length.

By means of the additional change of variable that follows,

$$Q_h' = Q_I' - nC_{ox}'\phi_t, \tag{10}$$

eqn.(9) becomes:

$$I_D = -\frac{\mu}{nC_{ox}'}\frac{W}{L}\int_{Q_F}^{Q_R}Q_h'dQ_h' = \frac{\mu}{C_{ox}'}\frac{W}{L}\frac{Q_F'^2 - Q_R'^2}{2n} \tag{11}$$

where Q_F' and Q_R' are the values of Q_h' evaluated at the source and drain ends, respectively (Table 2). In Fig.2, the current computed from eqn.(11), together with the approximation of Q_I' given in Table 1, is compared to that evaluated from the Brews model [1].

Through an analogous procedure, we find the total inversion (Q_I) and depletion (Q_B) charges:

$$Q_I = WL\left[\frac{2}{3}\frac{Q_F'^2 + Q_F'Q_R' + Q_R'^2}{Q_F' + Q_R'} + nC_{ox}'\phi_t\right] \tag{12.a}$$

$$Q_B = Q_P - \frac{n-1}{n}Q_I \tag{12.b}$$

where

$$Q_P = WL\left[Q_{BP}' + \frac{n-1}{n}Q_{IP}'\right] \tag{12.c}$$

and

$$Q_{BP}' = -C_{ox}'\gamma\sqrt{2\phi_F + V_P} \tag{12.d}$$

V - Small-Signal Parameters

The MOSFET intrinsic capacitances for quasi-static operation can be evaluated by differentiating the expressions of the total charges derived in the previous Section. The results are presented in Table 3.

In order to obtain the MOSFET source, drain and gate transconductances we have adopted the expression [1]:

$$I_D = \mu \frac{W}{L} \int_{V_{SB}}^{V_{DB}} (-Q_i') dV_{CB} \tag{13}$$

Differentiating eqn.(13) with respect to the terminal voltages, we find the expressions for the transconductances presented in Table 3.

We have used the relationship

$$\left.\frac{\partial Q_i'}{\partial V_G}\right|_{V_C, V_B} \equiv -\frac{1}{n}\left.\frac{\partial Q_i'}{\partial V_C}\right|_{V_G, V_B} \tag{14}$$

to derive the expressions for C_{gb} and g_{mG}. Eqn.(14) follows directly from the expressions of Q_i' in Table 1 for both strong and weak inversion if we assume that the variation of n with V_{GB} is negligible [4, 5].

The MOSFET small-signal parameters (Fig.3) are represented by simple and symmetric expressions that are accurate explicit functions of the terminal voltages if the inversion charge density and its derivatives are evaluated at drain and source according to the expressions derived in Section III. Tables 1, 2 and 3 summarize the formulae of the proposed model.

VI - Conclusions

We have accomplished a general explicit model very appropriate for the simulation of MOSFET circuits since the needs for simplicity and accuracy are simultaneously fulfilled. A smooth variation of the MOSFET characteristics, including small signal parameters, is

guaranteed through the entire inversion region (weak, moderate and strong inversion). Moreover, our model contains only the classical parameters of the MOSFET theory.

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Definitions and technological parameters regarding the figures:

$$u_{GB} = \frac{V_{GB}}{\phi_t} \qquad u_{CB} = \frac{V_{CB}}{\phi_t} \qquad u_{SB} = \frac{V_{SB}}{\phi_t} \qquad u_{DB} = \frac{V_{DB}}{\phi_t} \qquad u_{FB} = \frac{V_{FB}}{\phi_t} \qquad u_{T0} = \frac{V_{T0}}{\phi_t}$$

$$I_0 = \frac{\mu W}{L} C'_{ox} \phi_t^2 \qquad g_0 = \frac{\mu W}{L} C'_{ox} \phi_t$$

$N_A = 5 \times 10^{15}\ cm^{-3}$ $\qquad t_{ox} = 878$ Å $\qquad V_{FB} = -1.18$ V

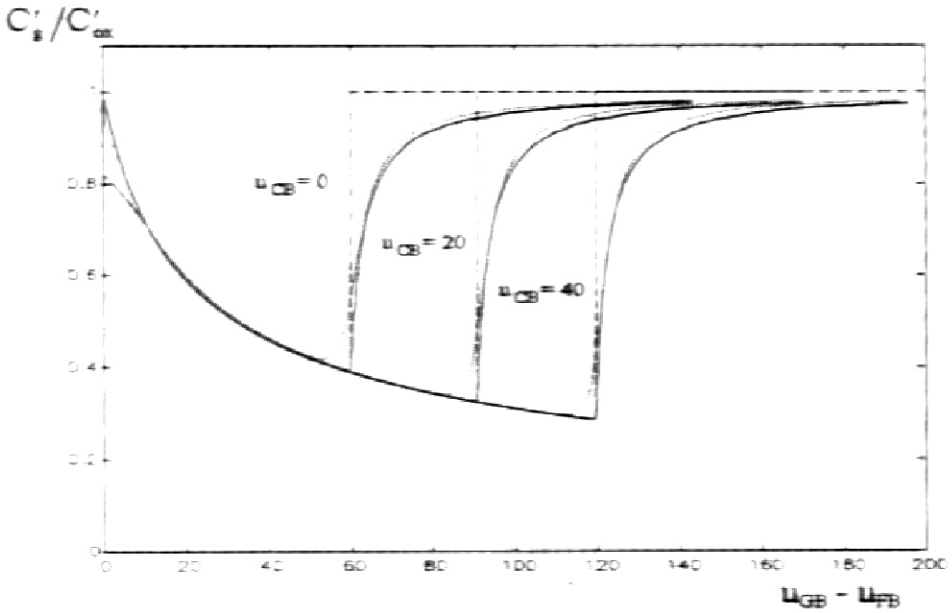

Fig. 1. Normalized gate capacitance per unit area versus normalized gate voltage computed from:

_____ Our model

. . . . . The theoretical implicit expression [1]

- - - - - - - The conventional model (infinite semiconductor capacitance in strong inversion)

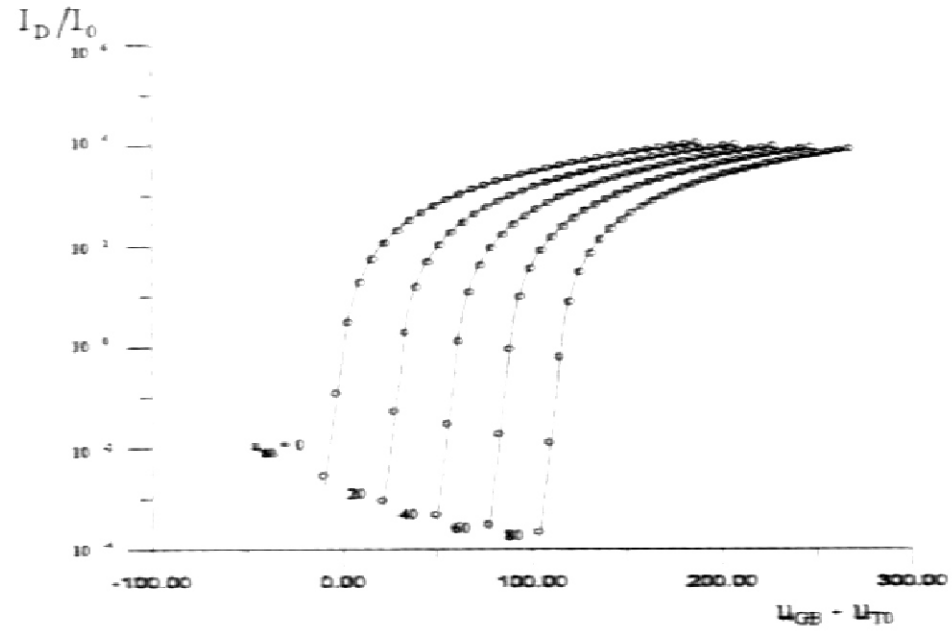

Fig. 2. Normalized drain current in saturation versus normalized gate voltage computed from:

_____ Our model - Table 1

o o o o o The charge-sheet model [1]

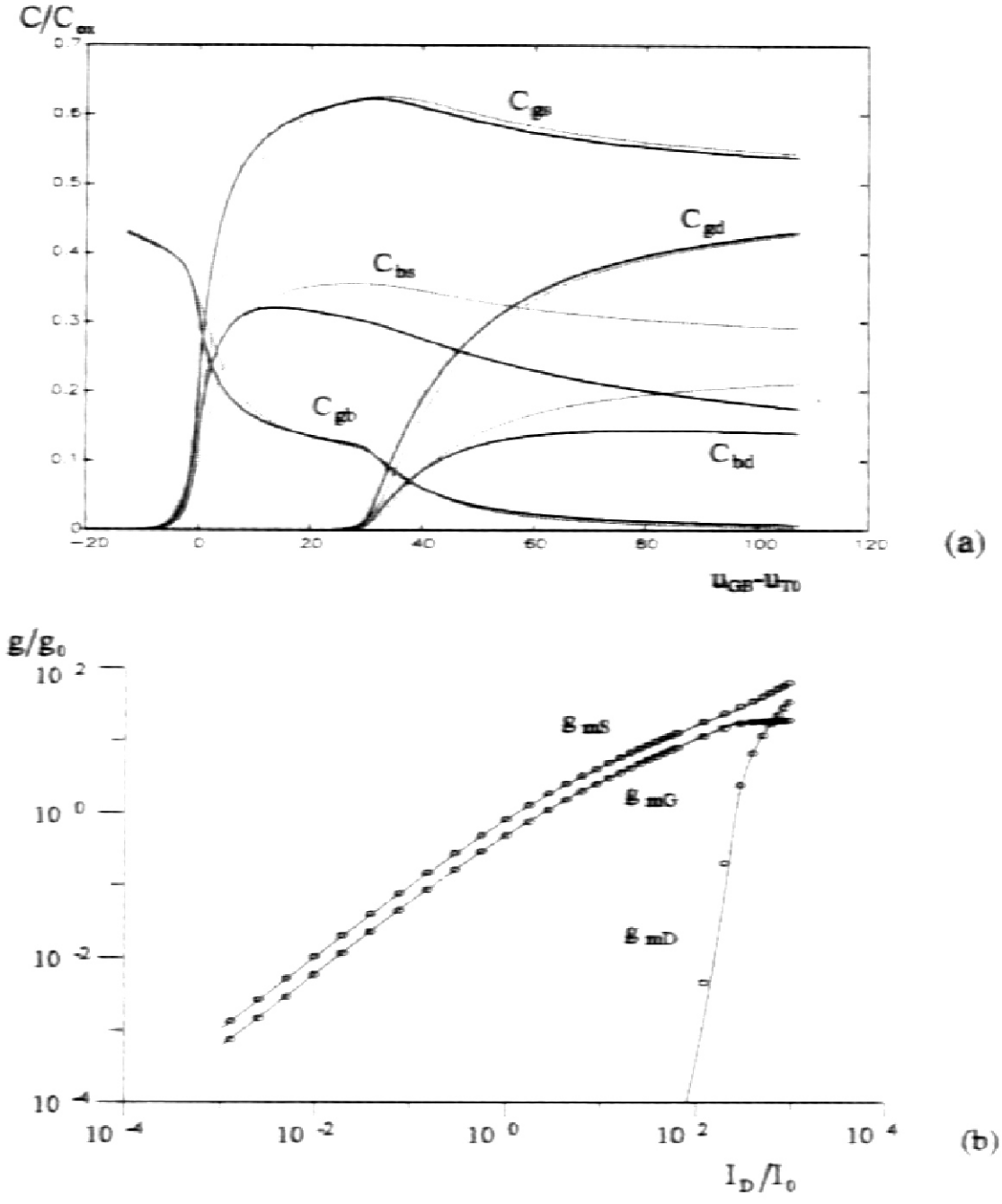

Fig.3. Normalized small-signal parameters for $u_{SB} = 0$ and $u_{DB} = 20$

(a) Intrinsic capacitances versus normalized gate-to-source voltage

(b) Transconductances versus normalized drain current

_____ Our model

. . . . . or o o o o o Charge sheet model

Table 1 - Inversion Charge Density (Q'_I) and Its Derivative (D') with respect to V_{CB}

	Expression	
Quantity	$V_{CB} \geq V_P$ (weak inversion)	$V_{CB} \leq V_P$ (strong inversion)
$Q'_I(V_{CB}, V_{GB})$	$-C'_{ox}(n-1)\phi_t e^{(V_P - V_{CB})/\phi_t}$	$-C'_{ox}\left[n(V_P - V_{CB}) - 2\phi_t \ln\left(1 + \frac{V_P - V_{CB}}{2\phi_t}\right) + (n-1)\phi_t\right]$
$D'(V_{CB}, V_{GB})$	$-Q'_I(V_{CB}, V_{GB})/\phi_t$	$C'_{ox}\left[n - \left(1 + \frac{V_P - V_{CB}}{2\phi_t}\right)^{-1}\right]$

Table 2 - Auxiliary Functions and Parameters

Quantity	Expression	Quantity	Expression	Quantity	Expression
Q'_F D'_F	$Q'_I(V_{SB}, V_{GB}) - nC'_{ox}\phi_t$ $D'(V_{SB}, V_{GB})$	A	$\frac{2}{3}WL$	n	$1 + \frac{\gamma}{2\sqrt{2\phi_F + V_P}}$
Q'_R D'_R	$Q'_I(V_{DB}, V_{GB}) - nC'_{ox}\phi_t$ $D'(V_{DB}, V_{GB})$	B	$\frac{\mu W}{C'_{ox}L}$	V_P	$\left(\sqrt{V_{GB} - V_{FB} + \frac{\gamma^2}{4}} - \frac{\gamma}{2}\right)^2 - 2\phi_F$

Table 3 -Drain Current and Small Signal Parameters

Quantity	Expression	Quantity	Expression	Quantity	Expression
I_D	$B\frac{(Q'^2_F - Q'^2_R)}{2n}$	g_{mS}	$-BC'_{ox}(Q'_F + nC'_{ox}\phi_t)$	g_{mD}	$-BC'_{ox}(Q'_R + nC'_{ox}\phi_t)$
C_{gs}	$\frac{A}{n}\left[1 - \frac{Q'^2_R}{(Q'_F + Q'_R)^2}\right]D'_F$	C_{gd}	$\frac{A}{n}\left[1 - \frac{Q'^2_F}{(Q'_F + Q'_R)^2}\right]D'_R$	C_{gb}	$\frac{n-1}{n}(C_{ox} - C_{gs} - C_{gd})$
C_{bs}	$(n-1)C_{gs}$	C_{bd}	$(n-1)C_{gd}$	g_{mb}	$\frac{g_{mS} - g_{mD}}{n}$